

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

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Title of Invention

DIGITAL SIGNAL PROCESSOR BASED ON JUMPING
FLOATING-POINT ARITHMETIC

Application Number :

Confirmation Number:

First Named Applicant: Yung-Chun Lei

Attorney Docket Number: MTKP0068USA

Art Unit:

Examiner:

Search string: (5884092 or 5524089 or 20030236651).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

| init | Cite.No. | Patent No. | Date | Patentee | Kind | Class | Subclass |
|------|----------|------------|------------|---------------|------|-------|----------|
| /MY/ | 1 | 5884092 | 1999-05-16 | KIUCHI ET AL. | | | |
| /MY/ | 2 | 5524089 | 1996-06-04 | TAKANO | | | |

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

| init | Cite.No. | Pub. No. | Date | Applicant | Kind | Class | Subclass |
|------|----------|-------------|------------|---------------------------|------|-------|----------|
| /MY/ | 1 | 20030236651 | 2003-12-25 | MIYASAKA, SHUJI ET AL. | | | |

Signature

| Examiner Name | Date |
|-----------------|------------|
| /Michael Yaary/ | 03/26/2008 |